



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Eitaro OTSUKA et al.

Application No.: 10/826,365

Docket No.: 119431

Filed: April 19, 2004

For: REFRESH CONTROL AND INTERNAL VOLTAGE GENERATION IN
SEMICONDUCTOR MEMORY DEVICE

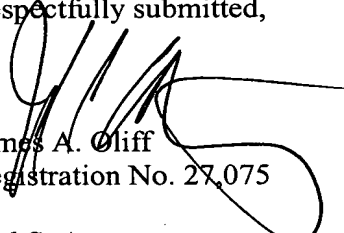
REQUEST FOR CORRECTION OF PALM RECORDS

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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,


James A. Oliff
Registration No. 27,075

Joel S. Armstrong
Registration No. 36,430

JAO:JSA/bmf

Date: August 30, 2004

OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

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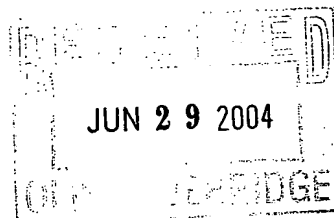
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLMS	IND CLMS
10/826,365	04/19/2004	2818	770	119431	13	9	2

CONFIRMATION NO. 1078

25944
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FILING RECEIPT



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Applicant(s)

Eitaro Otsuka, ~~Tokyo~~, JAPAN; Fujimi - machi;
Koichi Mizugaki, ~~Tokyo~~, JAPAN; Suwa - shi;

Assignment For Published Patent Application

SEIKO EPSON CORPORATION, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

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Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Refresh control and internal voltage generation in semiconductor memory device

Preliminary Class

365

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